



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/776,553  
Filing Date ..... February 10, 2004  
Inventor ..... Weimin Li et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 1755  
Examiner ..... Carol M. Koslow  
Attorney's Docket No. .... MI22-2497  
Title: ..... Compositions of Matter and Barrier Layer Compositions

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References –See Attached Form PTO/SB/08B

The attached Form PTO/SB/08B is submitted in compliance with 37 CFR §1.56. Pursuant to FEDERAL REGISTER, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

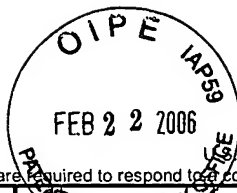
Respectfully submitted,

Dated:

*February 22, 2006*

By:

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PTO/SB/08B (08-03)

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STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/776,553
Filing Date	February 10, 2004
First Named Inventor	Weimin Li
Art Unit	1755
Examiner Name	Koslow, Carol M.
Attorney Docket Number	MI22-2497

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	A	Wolf Ph.D., Stanley, "Silicon Processing for the VLSI Era - Volume 2: Process Integration," ©1990, pages 48, 49 and 435.	
	B	Wolf Ph.D., Stanley, "Silicon Processing for the VLSI Era - Volume 1: Process Technology," ©1986, pages 407-413.	

Examiner Signature	Date Considered
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